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Sheet 1 of 1

FORM PTO-1449 (Rev. 2-32) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO.	SERIAL NO.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	D-8946	338111
	APPLICANT: LARRY F. WEBER, ET AL.	
	FILING DATE: Herewith	GROUP: 252

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

MBS			H. Tottori, E. Hatabe, F. Isogai and S. Yoshida, "A Driving Circuit for Plasma Display Panels", Society For Information Display, SID 75 Digest, Vol. 6, pp. 118-119, April, 1975.
MBS			W.E. Johnson, E.A. Oster and H.J. Hoehn, "Plasma Display/Memory Panel with Integral Drive Circuitry", Society For Information Display, SID 77 Digest, Vol. 8, pp. 20-21, April, 1977.
MBS			M.L. Higgins, "A Low-Power Drive Scheme for AC TFEL Displays", Society For Information Display, SID 85 Digest, Vol. 16, pp. 226-228, April-May, 1985.

EXAMINER	DATE CONSIDERED
SHINGLETON	11-1-90

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